Solutions to non-text problems in HW7

Problem 3:

Prob 3(a)
This can also be done without a multiplexer by using K-maps to find SOP logic for $J_i$ and $K_i$. The results are $J_i = S_1 S_0' + S_0 S_i$ and $K_i = S_1 S_0' + S_1'S_0 S_i$ using just 5 gates.