10. (Static Hazards)
   A) Given a function F, find all static hazard locations and mark with a “diamond.”

   B) Draw the logic gate circuit including the extra gates needed to eliminate the logic hazard in part a.

   C) You plan to implement the function represented in the K-MAP in Part A using sum of products. How many of the static hazards are static 0-hazards? How many are static 1-hazards?

   D) For your circuit designed in Part B, are there any dynamic hazards?
11. (Function Hazards) Indicate by double arrows the location of all function hazards in function F.