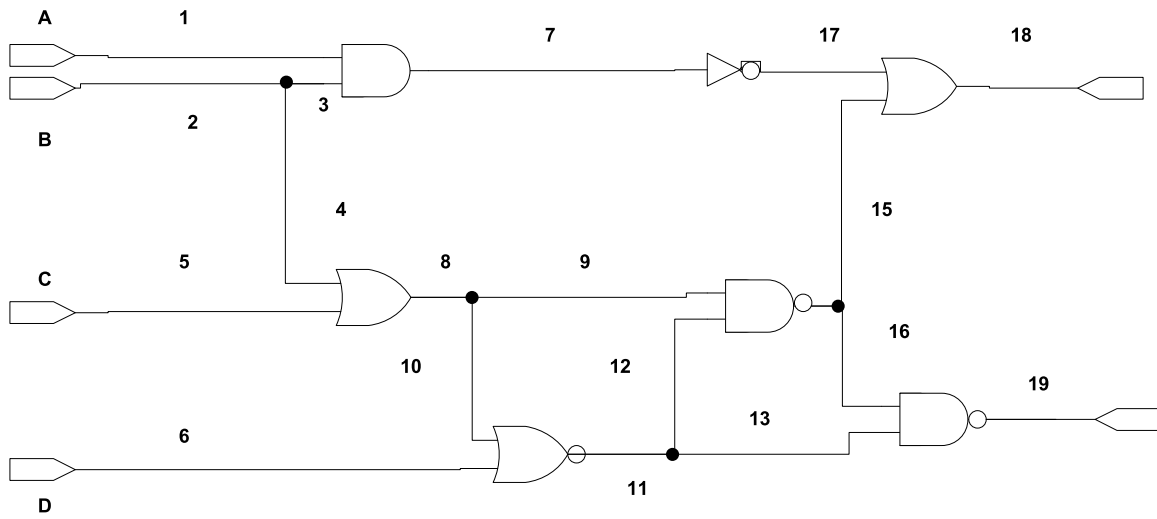


Fault	List all equivalent Faults
1/0	3/0,7/0
1/1	
2/0	
2/1	
3/1	
4/0	
4/1	
5/0	
6/0	
7/1	17/0
8/0	
9/0	
9/1	
10/0	
10/1	
11/1	
12/1	
13/1	
14/0	
15/0	
16/0	
16/1	
17/1	
18/0	
19/0	

Table 1: Table for problem 4(a), *Print this page, fill and submit with your work*

1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
2	0	0	0	0	1	1	1	1	0	0	0	0	1	1	1	1
3	0	0	1	1	0	0	1	1	0	0	1	1	0	0	1	1
4	0	1	0	1	0	1	0	1	0	1	0	1	0	1	0	1
1/0																
1/1																
2/0																
2/1																
3/1																
4/0																
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18/0																
19/0																

Table 2: Table for problem 4(b), *Print this page, fill and submit with your work*



Circuit for Problem 4

a combinational circuit consisting only of BOOLEAN gates are called the checkpoints. Thus, expend XOR gate as in Figure 4.9 in the text.

7. (10 points) (Bushnell and Agrawal) Problem 5.26.
(Read section 5.6.1 on the textbook)