

DISCUSSION SECTION SCHEDULE**Discussion Time:        Wednesday 5:00-6:30****Discussion Place:        EH3355**

The ECE 353 discussion section will consist of two major phases. The first phase of the discussion period will be used to explore one or more in-depth example(s) illustrating concepts being discussed in the lecture (the scheduled topics are listed in the below table, although there may be changes during the semester). The second phase of the discussion period will be centered on responding to specific inquiries or requests.

<b>Week #:</b>	<b>Date:</b>	<b>Topic:</b>
1	1/21/09	No Discussion the first week
2	1/28/09	Memory maps Microprocessor organization
3	2/4/09	ADuC7026 Overview Development Tools
4	2/11/09	Addressing modes Arithmetic and logical operations
5	2/18/09	Look-up tables Looping
6	2/25/09	Stacks and subroutines Register parameter passing
7	3/4/09	Stack frames
8	3/11/09	Decoding logic design Generic I/O device model
8.5	3/18/09	Spring Break (Discussion Held in Palm Springs)
9	3/25/09	Hardware timers
10	4/1/09	Memory system design
11	4/8/09	Demultiplexing Timing analysis for SRAM device
12	4/15/09	Interrupts and exceptions Switch debouncing
13	4/22/09	Software timers