

CONTACT INFORMATION	Room 3160, Wisconsin Institute for Discovery 330 N Orchard St, Madison WI 53705.	http://homepages.cae.wisc.edu/~sumeet/
RESEARCH INTERESTS	Preference learning, Sequential decision making, Active learning	
EDUCATION	<p>University of Wisconsin-Madison Ph.D, Electrical Engineering Aug 2012 - June 2018 (expected) Advisor: Prof. Rob Nowak GPA: 3.91/4.00</p> <p>University of Wisconsin-Madison M.S, Electrical Engineering (Communications/Signal Processing) Aug 2008 - Dec 2009 Advisors: Prof. Stark Draper, Prof. Ross Barmish GPA: 3.87/4.00</p> <p>College of Engineering Pune B.Tech, Electronics and Telecommunication Aug 2003 - May 2007 GPA: 8.91/10.00</p>	
PUBLICATIONS	<p><i>Conference Papers:</i></p> <ol style="list-style-type: none"> 1. Sumeet Katariya, Lalit Jain, Nandana Sengupta, James Evans, Robert Nowak <i>Adaptive Sampling for Coarse Ranking</i>, AISTATS 2018. 2. Sumeet Katariya, Branislav Kveton, Csaba Szepesvári, Claire Vernade, Zheng Wen, <i>Bernoulli Rank-1 Bandits for Click Feedback</i>, IJCAI 2017. 3. Sumeet Katariya, Branislav Kveton, Csaba Szepesvári, Claire Vernade, Zheng Wen, <i>Stochastic Rank-1 Bandits</i>, AISTATS 2017. 4. Alan Malek, Sumeet Katariya, Yinlam Chow, Mohammad Ghavamzadeh, <i>Sequential Multiple Hypothesis Testing with Type 1 Error Control</i>, AISTATS 2017. 5. Sumeet Katariya, Branislav Kveton, Csaba Szepesvári, Zheng Wen, <i>DCM Bandits: Learning to Rank with Multiple Clicks</i>, ICML 2016. 6. Sumeet Katariya, Kevin Jamieson, Atul Deshpande, Rob Nowak, <i>Sparse Dueling Bandits</i>, AISTATS 2015, Oral presentation. 7. S.-T. Zhou, S. Katariya, H. Ghasemi, S. C. Draper, and N. Kim: <i>Minimizing total area of low-voltage SRAM arrays with joint optimization of cell size, redundancy, and ECC</i>. IEEE Symp. Comp. Design, Amsterdam, the Netherlands, Oct. 2010. <p><i>Journal Papers:</i></p> <ol style="list-style-type: none"> 1. Kim, N. S., Draper, S. C., Zhou, S. T., Katariya, S., Ghasemi, H. R., and Park, T. (2012). <i>Analyzing the impact of joint optimization of cell size, redundancy, and ECC on low-voltage SRAM array total area</i>, Very Large Scale Integration (VLSI) Systems, IEEE Transactions on, 20(12), 2333-2337. 	
PATENTS	<ol style="list-style-type: none"> 1. Systems and methods associated with sequential multiple hypothesis testing, US Patent Application 20170330114A1. 2. Sequential Hypothesis Testing in a Digital Medium Environment, US20170323329A1. 3. Media Transmission Over a Data Network, US 20120327943A1. 	

4. Directional Light Source using Refractive and Reflective Optics. US Patent Application 20110007512.
5. Apparatus for Efficiently Coupling Light from a Light Source into a Thin Object. US Patent Application 20110013418.
6. Directional Linear Light Source. US Patent Application 20110013387.
7. Polarized Linear Light Source. US Patent Application 20110182050.
8. Photoluminescent Light Source. US Patent Application 20110182055.

TEACHING

University of Wisconsin Madison

- Graduate Teaching Assistant, ECE* Jan 2014 - May 2014
Teaching assistant for ECE 830: Estimation and Detection Theory (graduate course)
- Graduate Teaching Assistant, ECE* Jan 2014 - May 2014
Teaching assistant for ECE 203: Introduction to Signals, Information and Computing (98 students).
- Graduate Teaching Assistant, CS* Aug 2013 - Dec 2013
Teaching assistant for CS 368: Learning a New Programming Language - Matlab (43 students).
- Graduate Teaching Assistant, ECE* Jan 2009 - May 2009
Teaching assistant for ECE 332: Feedback Control Systems. Rating: 4.18/5.00 (23 students)

INTERNSHIPS

Lands' End

- Data Science Intern, Center for Innovation* Aug 2017-May 2018
- Develop a deep-learning based fashion recommendation engine.

Adobe Research

- Data Science Intern, Big Data Lab* Sep 2015 - Dec 2015
- Sequential multiple hypothesis testing for Adobe Target.

Facebook

- SDE Intern, Entities* May 2013 - Aug 2013
- Using social signals to infer location information of people's workplaces.

OPEN SOURCE

Google Summer of Code

- Software Developer, Scilab* May 2012 - Aug 2012
- Developed a Signal Processing Blockset for Xcos (Scilab's modeling language analogous to Simulink).

WORK EXPERIENCE

Oneirix Labs (Startup)

- Research Scientist* <https://www.oneirix.com>
May 2007 - July 2008, Jan 2010 - May 2012
- *Research:* Electromagnetics, Optics, LCD backlight modeling, simulation and prototyping
 - *Teaching:* Conducted short courses on Advanced DSP, Convex Optimization, Linear Dynamical Systems, Information Theory, Probability and Statistics.
 - *Miscellaneous:* Patent prosecution, mentoring interns, recruiting candidates.

PROGRAMMING

Python, C++, Octave/Scilab/Matlab.

AWARDS

- AISTATS 2017, ICML 2016 Travel Award
- Recipient of University of Wisconsin-Madison's Donald and Esther Procknow Fellowship for the year 2012-13.
- Recipient of the Kishore Vaigyanik Protsahan Yojana (KVPY) Fellowship from 2004-2007.
- Won 1st prize at Techfest, IIT Bombay (India's largest Inter-Collegiate Robotics Fest).
- All India Rank 8 in GATE-Mathematics (equivalent of the Math subject GRE).

SOCIAL SERVICE

- [Pankhudi Foundation](#): Creatively taught math and science to underprivileged kids.
- [Nirman](#): Worked under the guidance of Abhay and Rani Bang (Padma Shri awardees) to understand problems of the tribal community in Gadchiroli.